Yang Li

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Education

Carnegie Mellon University **09/2013 - Present**
Ph.D. in Electrical and Computer Engineering
- Advisor: Onur Mutlu
- Research Area: computer system/architecture, cloud computing, power management
- Relevant Courses: computer architecture, parallel computer architecture, database

The University of Texas at Austin **08/2011 - 05/2013**
M.S. in Electrical and Computer Engineering
- Overall GPA: 3.85
- Relevant Courses: machine learning, reinforcement learning, convex optimization

Tsinghua University, Beijing, China **08/2007 - 07/2011**
B.E. in Automation
- Overall GPA: 88/100
- Relevant Courses: computer networks, automatic control theory, process control, system identification, data structures and algorithms, graph theory

Industry Experience

Research Intern, IBM Austin Research Lab **05/2016 – 12/2016**
Mentors: Charles Lefurgy, Karthick Rajamani
- Proposed a data center-level power capping algorithm and system that addresses the challenges arising from real-world cloud infrastructures. Contributed to multiple IBM IPs.

Research Intern, Microsoft Research **06/2014 – 08/2014**
Mentors: Jie Liu, Di Wang
- Worked on power management for fuel cell powered data center. Proposed design guidelines for energy storage device sizing and power capping. **[HPCA'16]**

Selected Publications

- **[HPCA'16].** Y. Li, D. Wang, S. Ghose, J. Liu, S. Govindan, S. James, E. Peterson, J. Siegler, R. Ausavarungnirun, and O. Mutlu. SizeCap: Efficiently Handling Power Surges for Fuel Cell Powered Data Centers. *IEEE Symposium on High Performance Computer Architecture* (HPCA), Mar. 2016.
- **[SAFARI Technical Report'15].** Y. Li, J. Choi, J. Sun, S. Ghose, H. Wang, J. Meza, J. Ren, and O. Mutlu. Managing Hybrid Main Memories with a Page-Utility Driven Performance Model. *SAFARI Technical Report* No. 2015-010, Jul. 2015.

Other Publications

- **[TVLSI'15].** Z. Ye, T. Wang, and Y. Li. Domain-Alternated Optimization for Passive Macromodeling. *IEEE Transactions on Very Large Scale Integration Systems*, Oct. 2015.
- **[DAC'13].** Y. Li, and D. Z. Pan. An Accurate Semi-Analytical Framework for Full-Chip TSV-induced Stress modeling. *IEEE/ACM Design Automation Conference*, Jun. 2013.
- **[DAC'13].** Z. Ye, B. Wu, S. Han, and Y. Li. Time-Domain Segmentation based Massively Parallel Simulation for ADCs. *IEEE/ACM Design Automation Conference*, Jun. 2013.
- **[DAC'11].** Z. Ye, Y. Li, M. Gao, and Z. Yu. A Novel Framework for Passive Macromodeling. *IEEE/ACM Design Automation Conference*, Jun. 2011.

Technical Skills

- **Programming:** C, C++, C#, Python, UNIX Shell
- **Database:** SQL, Cassandra
- **Computing Framework:** Apache Spark
- **Others:** MATLAB, Verilog

Awards

- **IBM Patent Application Award**. *IBM*, Dec. 2016.
- **Carnegie Institute of Technology Dean's Tuition Fellowship**, *Carnegie Mellon University*, Sept. 2013.
- **TI Outstanding Student Designer Award**, *Texas Instrument*, May 2013.
- **Microelectronics and Computer Development Fellowship**, *The University of Texas at Austin*, Aug. 2011.
- **Best Undergraduate Thesis Award**, *Tsinghua University*, Jul. 2011.
- **Bronze Medal of Chinese Physics Olympiad**, *Chinese Physics Society*, Nov. 2006.