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Joohyun Lee received the MS degree in electrical engineering from Pohang University of Science and Technology (POSTECH), Korea, in 1998. In 1998, he joined the advanced memory design team of Hynix, Korea. Since 2000, he has been with ETRI, Korea. His research interests include mobile communication modem and brain-inspired neuromorphic SoC. He also interested in deep-learning algorithm and realizing human-like visual intelligence in a chip.