Speaker Profile



Dr. Robert S. Chau

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Robert S. Chau is an Intel Fellow and Director of Transistor Research. He is responsible for directing research and development in advanced transistors and gate dielectrics for microprocessor applications. He is also responsible for leading research efforts in advanced nanotechnology for future device and process applications.

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Dr. Chau joined Intel in 1989 and developed seven generations of Intel gate oxides along with many transistor innovations used in various Intel logic products. He also introduced many new process modules and electrical test capability for Intel's future logic processes. Dr. Chau was promoted in 2000 to the rank of Intel Fellow, the company's highest and most prestigious technical position.

Dr. Chau currently manages the Novel Device Laboratory and leads a research team focusing on new transistor architectures, process modules and technologies, and characterization techniques for the 45nm and 30nm logic technology nodes and beyond. He is also the high-K program manager responsible for the research and development of alternative gate dielectric stacks.

Dr. Chau received his bachelor's and master's degrees and Ph.D. in electrical engineering from The Ohio State University. He holds 37 United States patents in device and process technologies, and has received five Intel Achievement Awards and 13 Intel Logic Technology Development Division Recognition Awards for his outstanding technical achievements in gate oxide, transistor and process module research and development.

Dr. Chau has received the 2003 Alumni Professional Achievement Award from The Ohio State University Alumni Association. He was born in Hong Kong in 1960.